

**IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Nicholas J. Kelsey, et al.
SERIAL NO.: 09/748,098
FILING DATE: December 21, 2000
TITLE: System and Method for Instruction Level Multithreading in an
Embedded Processor Using Zero-Time Context Switching
EXAMINER: David J. Huisman
GROUP ART UNIT: 2183
ATTY. DKT. NO.: 20880-05093

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

37 CFR §1.121

SIR:

In response to the Notice of Non-Compliant Amendment mailed March 19, 2007,
Applicants submit the following Amendments and Remarks, which now include the proper
status identifier for each claim.